

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: M. HORIUCHI, et al.
Application No.: Not assigned yet
Filed: On even date herewith
For: SEMICONDUCTOR DEVICE AND MANUFACTURING
METHOD THEREOF
Art Unit: Not assigned yet
Examiner: Not assigned yet

PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. BOX 1450
Alexandria, VA 22313-1450

March 3, 2004

Sir:

Prior to examination, please amend the above-identified application as listed below and as set forth on the following pages:

Amendments to the Claims; and

Remarks are included following the amendment.